Reviewer #1: Some basic commons:  
1. LVDS is a protocol of the transfer LEVEL, it's not good to say transfer xxx through LVDS

A: This part has been rewritten as below:

… directly based on the Low Voltage Differential Signal (LVDS) standard.

2. Normally, clock not one part of the fast control

A: the clock has been removed from the fast control. It has been rewritten as below:

… include SKIROC2’s trigger and reset or validity of the SCA…

3. It's quite important of the Voltage of the system, including the HV for sensor and LV for electronics, need some description.

A: The description about voltage of the system has been added to the paper:

1. The SKIROC2 requires a supply voltage of + 3.3 V, which is supported by a Low-DropOut (LDO) regulator from a primary supply of + 5V.
2. The cathode of the diode is connected to a bias voltage of +23 V supported by a well-designed LDO regulator with an initial supply of + 25 V from outside the board.

Reviewer #2: This paper has reported the development of a SKIROC2 based prototype electronics system for Silicon PIN array. Both system design and performance evaluation are presented. The text quality needs to be revised slightly before publication. The following are comments and suggestions to the authors.  
  
1. English: there are few English language issues in the paper, please consider revising and polishing the text. The following are some obvious ones.

Page 1  
line 47: … and the ILC, "require" finer granularity…  
A: This part has been rewritten as below:

… and require finer granularity …  
  
Page 2  
Line 7-10: The sentence needs to be reorganized.

A: The sentence is reorganized as below:

In this paper, a prototype of multi-channel electronics system, which is based on the SKIROC2 ASIC, is designed and implemented. The system is also considered as the pre-research of the Silicon Tungsten (Si-W) ECAL for CEPC.

line 18: … and "processes" the signals as well.

A: The sentence has been rewritten as below:

… and processes the signal …  
  
Page 3  
line 29: …and "precision" measurement,…

A: This part has been rewritten as below:

… and precision measurement …

line 37: The "peaking" time…

A: This part has been rewritten as below:

The peaking time …

line 41: …different "experimental" conditions.

A: This part has been rewritten as below:

… different experimental conditions …

line 42" …diode array in "different" experiments,…

A: This part has been rewritten as below:

… diode array in different experiments …

line 50:…make the SKIROC2 "function" well.  
A: This part has been rewritten as below:

… make the SKIROC2 function well …

Page 4  
line 10: The sentence needs to be reorganized.

A: This part has been reorganized as below:

The fast control signals, which include SKIROC2’s trigger and the reset or validity of the SCA, are sent to the control center of SKIROC2 directly based on the Low Voltage Differential Signal (LVDS) standard.

line 42: power supply rejection "ratio".

A: This part has been rewritten as below:

…a well-designed LDO regulator with an initial supply of +25 V from outside the board….

line 53: …in charge of "communication" with…  
A: This part has been rewritten as below:

… in charge of communication with …

Page 5  
line 8: is "implemented" for compatibility…

A: This part has been rewritten as below:

… is implemented for …

line 32: receiving commands "from" and …

A: This part has been rewritten as below:

… receiving commands from and …

line 49: …normal acquisition "process"…

A: This part has been rewritten as below:

… normal acquisition process …   
  
Page 6  
line 12: from DIF to SKIROC2, "to start" the readout phase.

A: This part has been rewritten as below:

… to start the readout phase…

line 42: an analog probe is "available" to observe…

A: This part has been rewritten as below:

… is available to observe …  
  
Page 8  
line 20: The calibration "procedure" …

A: This part has been rewritten as below:

The calibration procedure …

line 27: …for performance "evaluation".

A: This part has been rewritten as below:

… for performance evaluation …  
  
Page 9  
line 14: …every channel should help get a better…

A: This part has been rewritten as below:

… every channel should help get a better …

line 39: …the left "plot"…

A: This part has been rewritten as below:

… the left plot …

line 42: The right "plot" of …  
… the right plot …

Page 10  
line 14: …be applied "to" the preliminary…

A: This part has been rewritten as below:

… be applied to the preliminary …

line 22: …system design "process".

A: This part has been rewritten as below:

… system design process …

line 23: …during the design "phase",…

A: This part has been rewritten as below:

… during the design phase …

2. Please consider to make consistent naming style. For example, here are different variants, Detector\_Part, Detector-Part, Detector part.

A: All the variants have been changed to Detector-Part and ASIC-Part.  
  
3. Please consider to adjust font size to be the same. For example, Fig. 6 and Fig. 7 in the text are in different font size.

A: The font size of Fig. 6 and Fig. 7 has been adjusted to the same.  
  
4. Please consider to use similar tense, preferably with present tense instead of past tense.  
A: All the past tense parts have been changed to the present tense.

5. Fig. 8 shows pedestal and noise distribution without detector, can one explain why some channels have low pedestal in left plot, and some channels have high noise in the right plot?  
A: I checked the raw data and found that the 4 channels with low pedestal were just the ones with high noise level. Further tests proved that the 4 channels’ performance were worse than the others.

6. Section 3.3, it is not clear from the description if the calibration pulse will be injected to all channels at the same time, or it is possible to inject charge to individual channels. Can one elaborate?

A: This part has been added to the paper to describe the calibration method.

A waveform generator with attenuator is used to generate step pulses with different amplitudes. This voltage pulse is sent into the SKIROC2 through the calibration pin and applied to all the calibration capacitors at the same time. When the step pulses are applied to the on-chip capacitor, a certain amount of charge, which covered the full range, is injected into every channel of SKIROC2 for performance evaluation.